

ABSTRACT OF THE DISCLOSURE

A liquid crystal display panel driving circuit includes a plurality of data signal lines, a plurality of data lines, a plurality of data switches, each data switch connecting at least one data signal line to the plurality of data lines, a plurality of pixels, a plurality of pixel switches connecting a data signal transmitted on each data line to at least one of the pixels, and a plurality of capacitors, each capacitor connected to at least one of the data lines for storing a voltage corresponding to the data signal transmitted by one of the data switches and for transmitting the voltage to one of the pixels.

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